

FIG. 1 is a block diagram of a computer system 110. The system 110 includes a processing unit 120, system memory 130, and various interfaces and peripherals. The system memory 130 is divided into system memory (ROM) 131 and system memory (RAM) 132. The system memory (ROM) 131 contains BIOS 133. The system memory (RAM) 132 contains operating system 134, application programs 135, other program modules 136, and program data 137. The processing unit 120 is connected to the system memory 130 via a bus 121. The processing unit 120 is also connected to an output peripheral interface 190, which is connected to a monitor 191, printer 196, and speakers 197. The processing unit 120 is connected to a video interface 195, which is connected to a video display 196. The processing unit 120 is connected to a network interface 170, which is connected to a local area network 171 and a wide area network 173. The processing unit 120 is connected to a user input interface 160, which is connected to a keyboard 162, pointing device 161, and microphone 163. The processing unit 120 is connected to a removable non-volatile memory interface 140, which is connected to a non-volatile memory interface 151. The non-volatile memory interface 151 is connected to a non-volatile memory 155, which is connected to a removable non-volatile memory 156. The processing unit 120 is connected to a program data 147, which is connected to a program data 146. The processing unit 120 is connected to a program data 145, which is connected to a program data 144. The processing unit 120 is connected to a program data 143, which is connected to a program data 142. The processing unit 120 is connected to a program data 141, which is connected to a program data 140. The processing unit 120 is connected to a program data 139, which is connected to a program data 138. The processing unit 120 is connected to a program data 137, which is connected to a program data 136. The processing unit 120 is connected to a program data 135, which is connected to a program data 134. The processing unit 120 is connected to a program data 133, which is connected to a program data 132. The processing unit 120 is connected to a program data 131, which is connected to a program data 130. The processing unit 120 is connected to a program data 129, which is connected to a program data 128. The processing unit 120 is connected to a program data 127, which is connected to a program data 126. The processing unit 120 is connected to a program data 125, which is connected to a program data 124. The processing unit 120 is connected to a program data 123, which is connected to a program data 122. The processing unit 120 is connected to a program data 121, which is connected to a program data 120. The processing unit 120 is connected to a program data 119, which is connected to a program data 118. The processing unit 120 is connected to a program data 117, which is connected to a program data 116. The processing unit 120 is connected to a program data 115, which is connected to a program data 114. The processing unit 120 is connected to a program data 113, which is connected to a program data 112. The processing unit 120 is connected to a program data 111, which is connected to a program data 110.

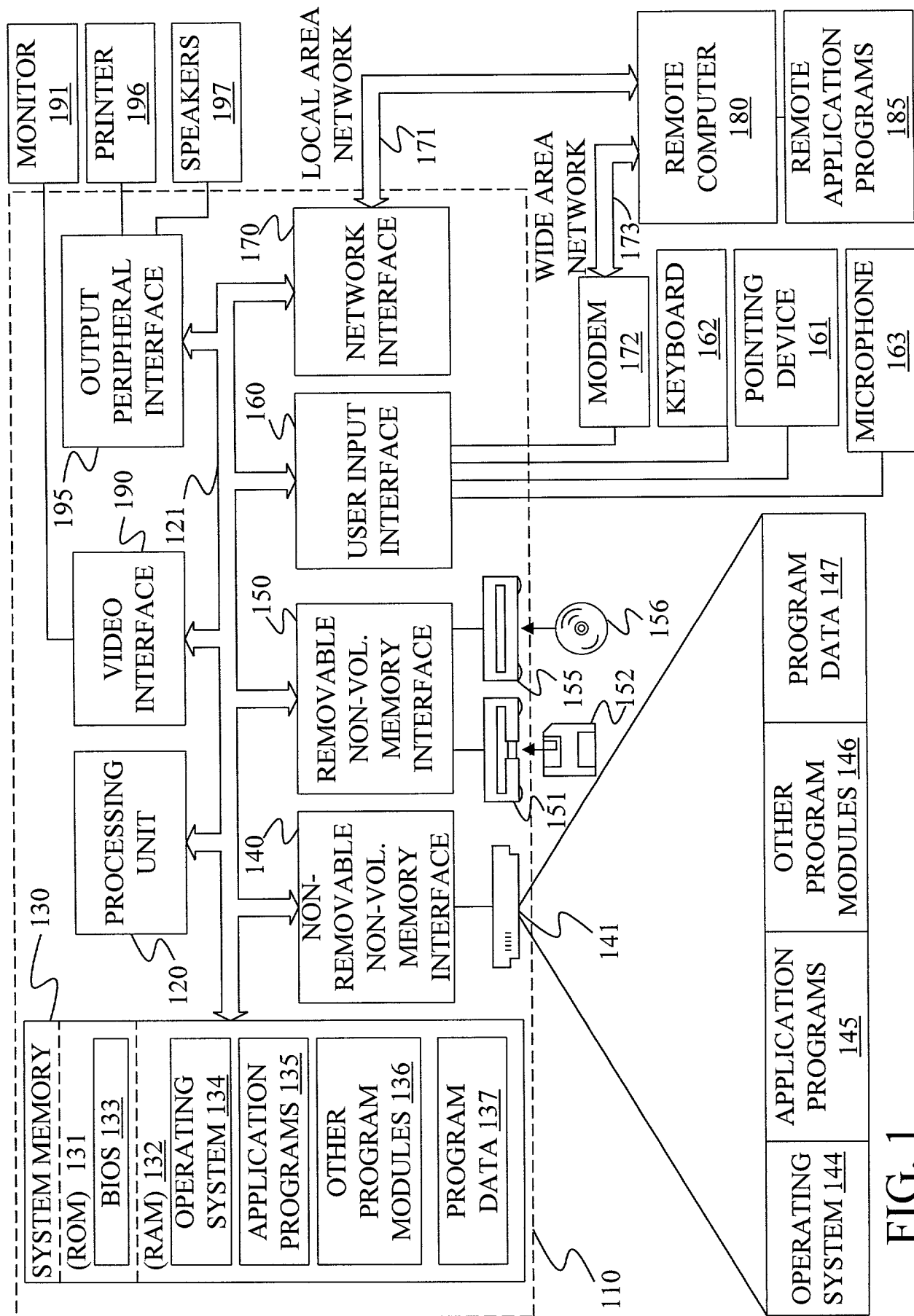


FIG. 1

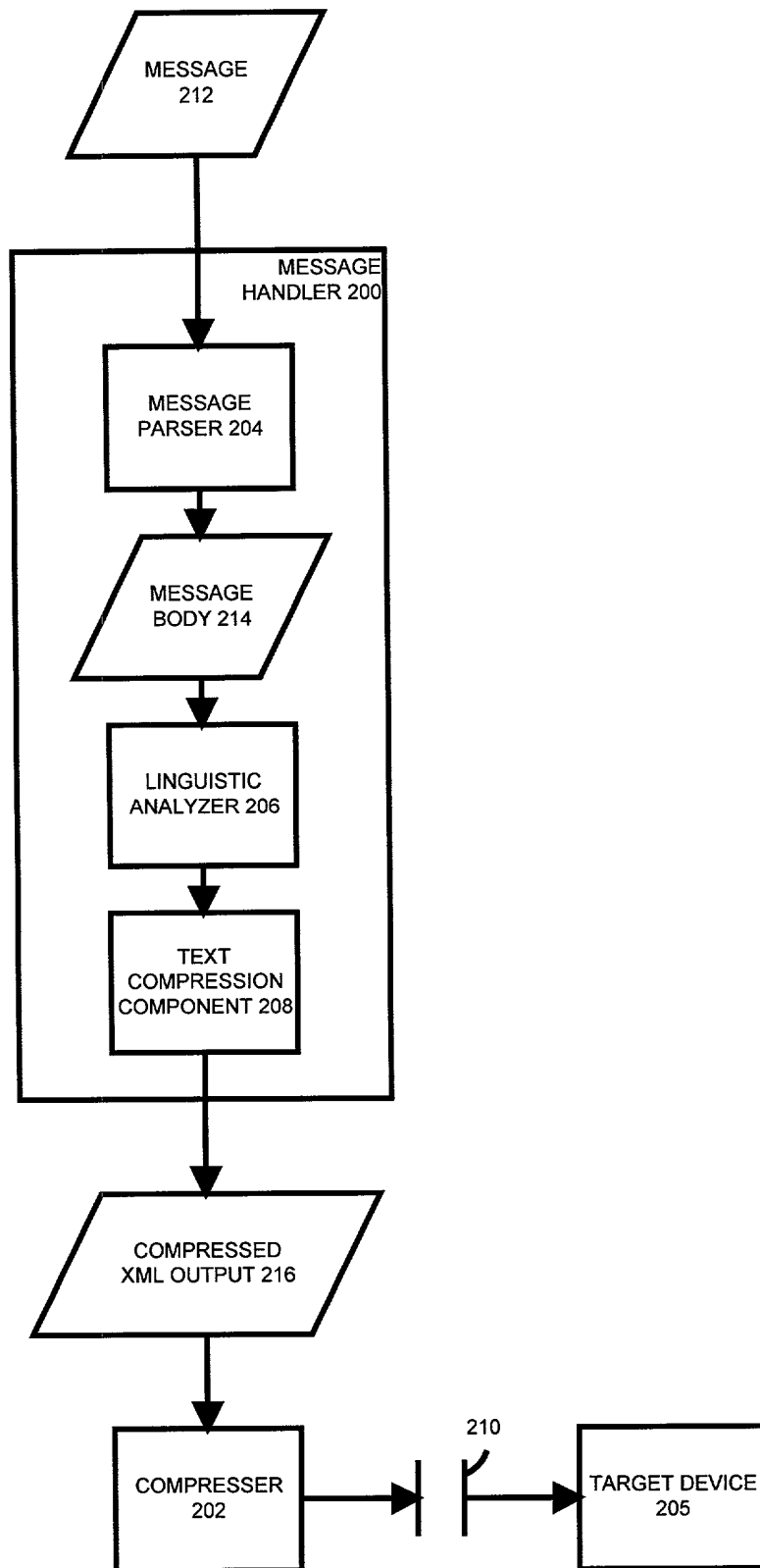


FIG. 2

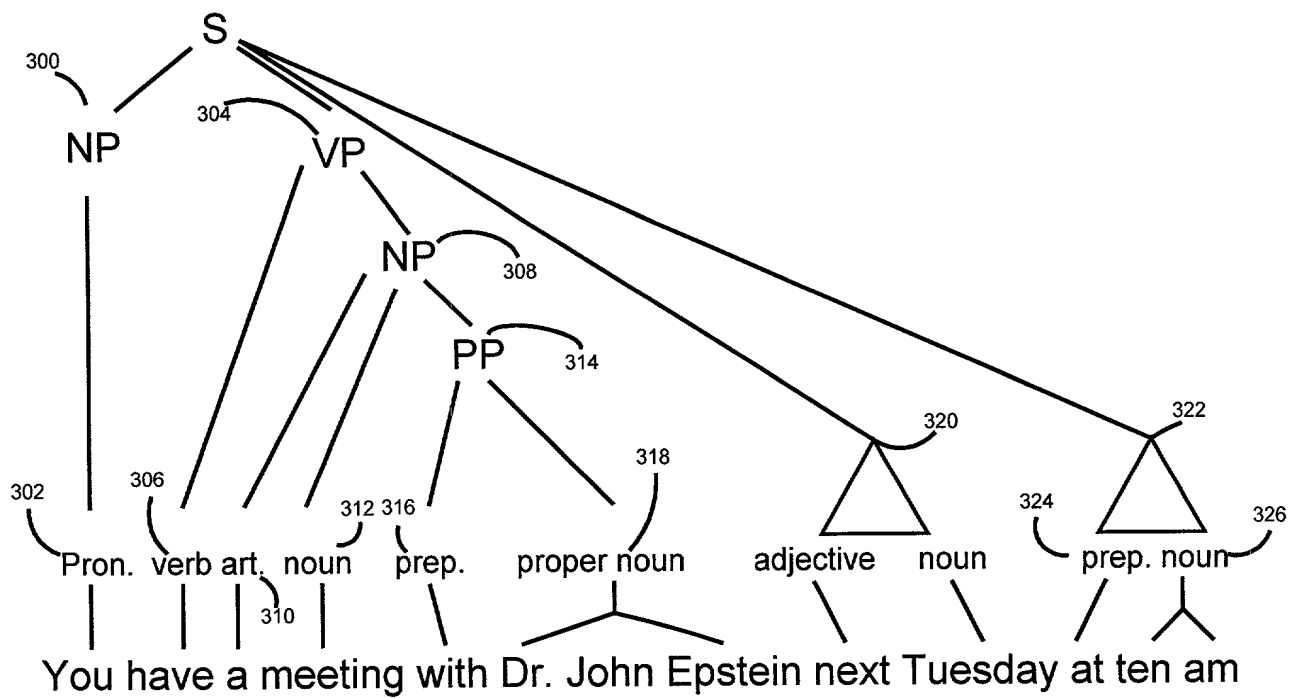


FIG. 3

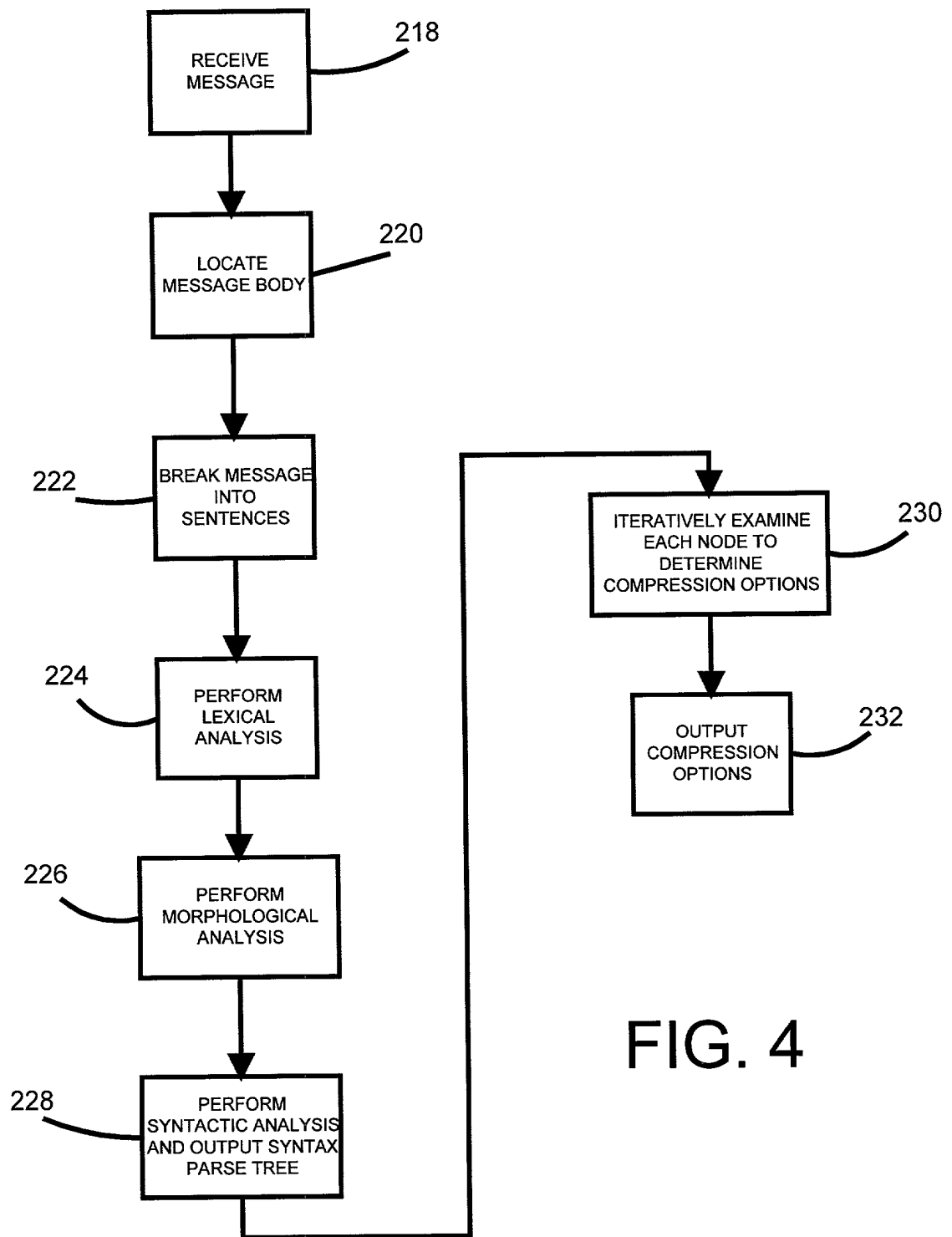
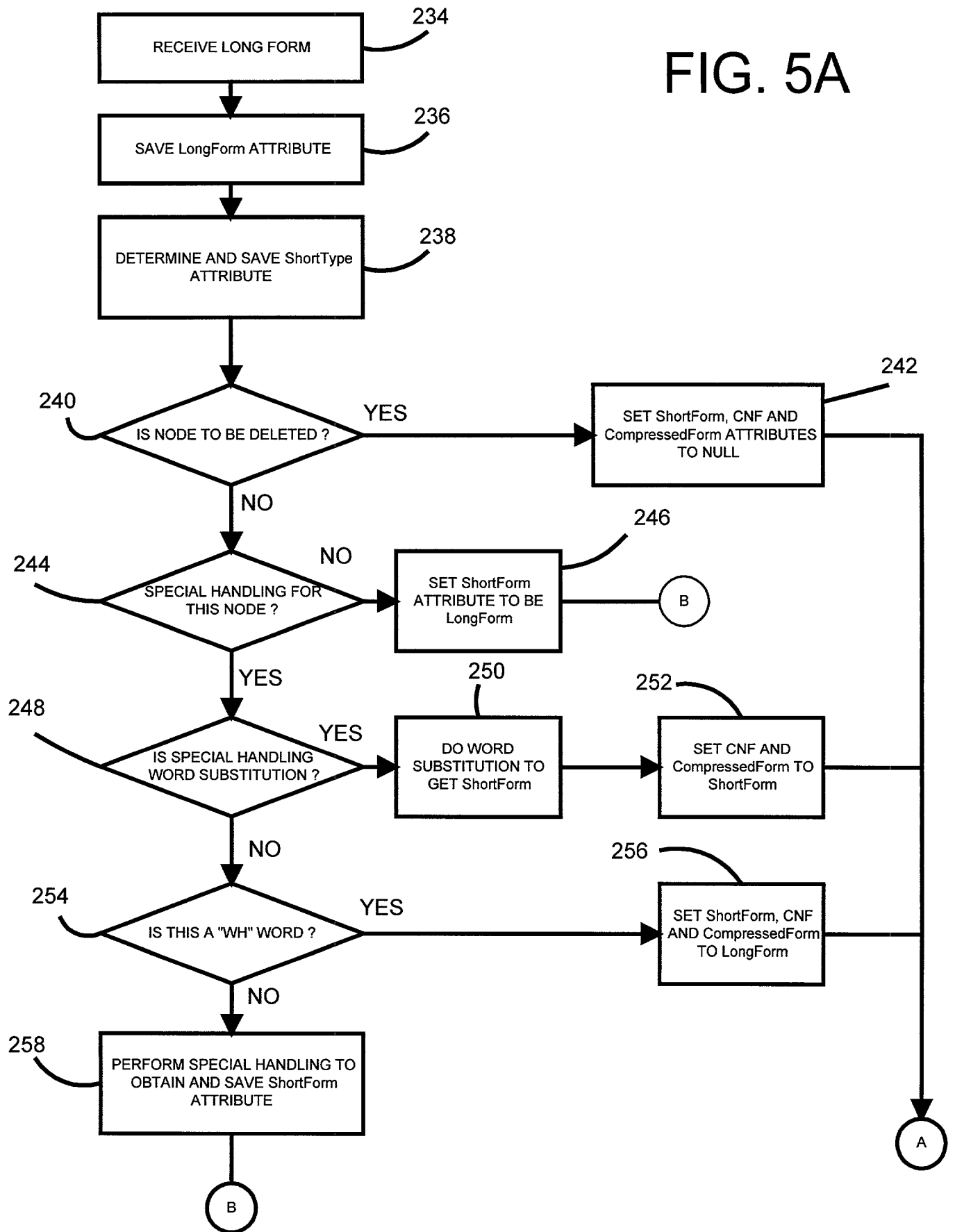


FIG. 4

FIG. 5A



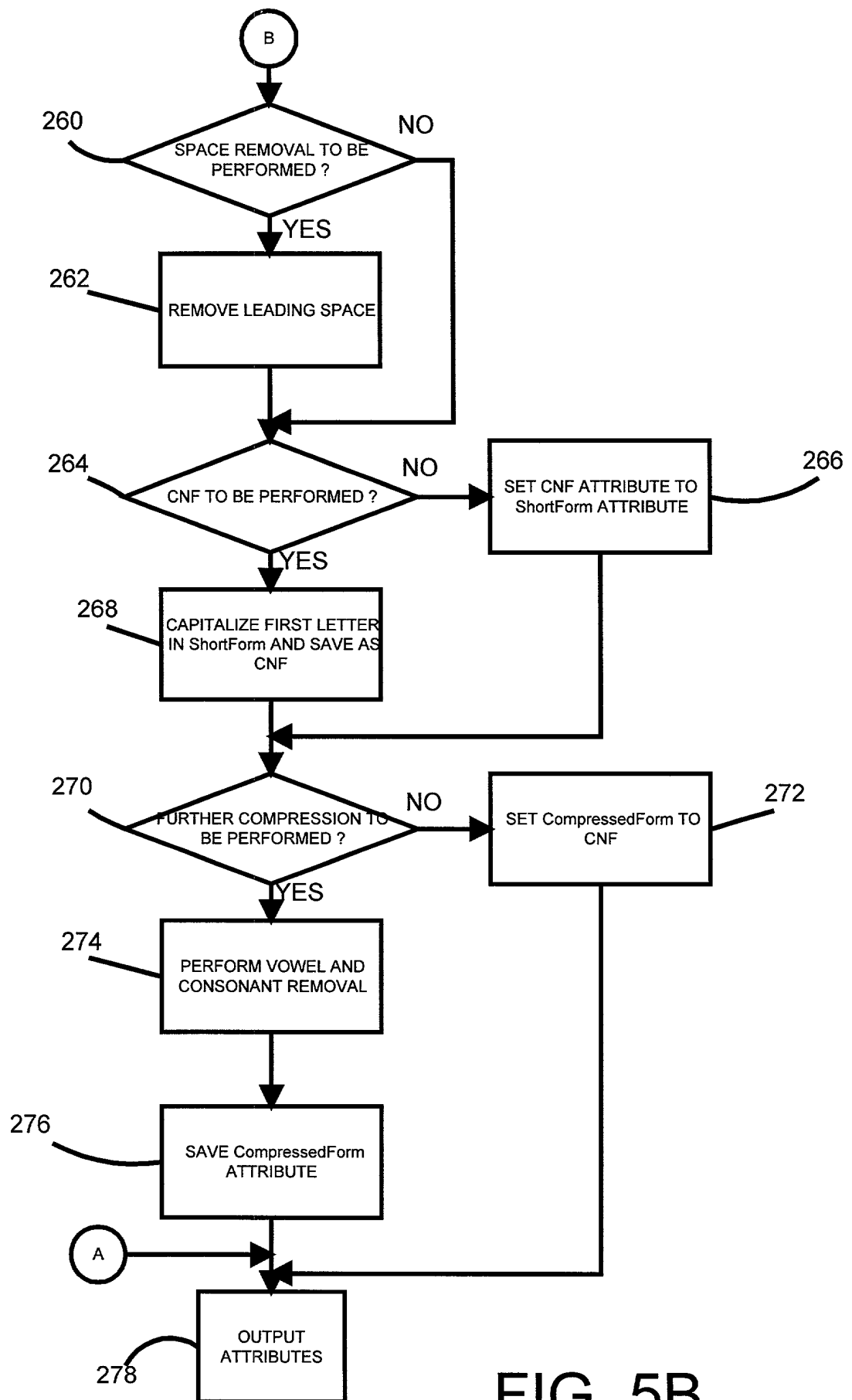


FIG. 5B